


ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Content Addressable Memory (CAM) Devices Having Soft Priority Resolution Circuits Therein and Methods of Operating Same						
<div style="display: flex; justify-content: space-between; align-items: flex-start;"> <div> <p>Application Number : 10/613542</p> <p>Confirmation Number: 4532</p> <p>First Named Applicant: Kee Park</p> <p>Attorney Docket Number: 5646-60IP</p> <p>Art Unit: 2818</p> <p>Examiner:</p> <p>Search string: (6748484 or 6763426 or 6697276 or 5930359 or 20020161969).pn</p> </div> <div style="text-align: center;">  </div> </div>							
US Patent Documents Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
/RD/	1	6748484	2004-06-08	Henderson	B1	711	108
↓	2	6763426	2004-07-13	James	B1	711	108
↓	3	6697276	2004-02-24	Pereira	B1	365	49
↓	4	5930359	1999-07-27	Kempke	A	713	160
US Published Applications Note: Applicant is not required to submit a paper copy of cited US Published Applications							
init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
/RD/	1	20020161969	2002-10-31	Nataraj	B1	711	108
Signature							
Examiner Name				Date			
/Ryan Dare/				04/12/2008			

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /RD/